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TROP, PRUNER & HU, P.C.

INTELLECTUAL PROPERTY LAW ATTORNEYS

1616 South Voss Road, Suite 750  
Houston, Texas 77057-2631

Bus: (713) 468-8880

Fax: (713) 468-8883

# Fax

<b>To:</b>	Examiner Jung H. Hur	<b>From:</b>	Timothy N. Trop
<b>Company:</b>	US Patent Office	<b>Date:</b>	November 28, 2006
<b>Fax:</b>	(571) 273-8300	<b>Pages:</b>	8 (including cover sheet)
<b>Your Re:</b>	10/712,205	<b>Our Re:</b>	ITL 1008US

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Applicant:	§	
Ebrahim Andideh et al.	§	Art Unit: 2824
	§	
Serial No.: 10/712,205	§	Examiner: Jung H. Hur
	§	
Filed: November 12, 2003	§	Atty Docket: ITL.1008US
	§	P15533
For: Parallel Electrode Memory	§	
	§	Assignee: Intel Corporation
	§	

Mail Stop AF  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**REPLY TO FINAL REJECTION**

Sir:

In response to the final rejection mailed November 7, 2006, reconsideration is requested in view of the following remarks.

A current listing of the claims is attached. No amendments were made.

Date of Deposit: November 28, 2006  
I hereby certify that this document is being facsimile transmitted to the  
United States Patent and Trademark Office (Fax No. 571/273-8300) on the  
date indicated above.  
*Cynthia L. Hayden*  
Cynthia L. Hayden